16.317: Microprocessor-Based Systems I

Fall 2012

Exam 3 December 14, 2012

Name: ID #:

For this exam, you may use a calculator and one 8.5" x 11" double-sided page of notes. All other electronic devices (e.g., cellular phones, laptops, PDAs) are prohibited. If you have a cellular phone, please turn it off prior to the start of the exam to avoid distracting other students.

The exam contains 3 questions for a total of 100 points. Please answer the questions in the spaces provided. If you need additional space, use the back of the page on which the question is written and clearly indicate that you have done so.

Please note that Question 3 has three parts, but you are only required to complete two of the three parts. You may complete all three parts for up to 10 points of extra credit. If you do so, please clearly indicate which part is the extra one—I will assume it is part (c) if you mark none of them.

You will be provided with six pages (3 double-sided sheets) of reference material: a list of the 80386 instructions we covered, as well as the PIC 16F684 instruction set, memory map, and block diagram. You do not have to submit these pages when you turn in your exam.

You will have three hours to complete this exam.

Q1: Multiple choice	/ 20
Q2: PIC programming	/ 40
sequences	7 10
Q3: Complex operations	/ 40
TOTAL SCORE	/ 100
EXTRA CREDIT	/ 10

1. (20 points, 5 points per part) *Multiple choice*

For each of the multiple choice questions below, clearly indicate your response by circling or underlining the single choice you think best answers the question.

- a. When writing PIC 16F684 assembly code, which of the following multi-byte operations will use the carry bit (C) to transfer information from one byte to the next?
 - A. 16-bit addition
 - B. 32-bit AND
 - C. 32-bit left shift
 - D. 16-bit XOR
 - i. Only A
 - ii. A and B
- iii. A and C
- iv. B and C
- v. B and D
- b. As discussed in class, many of the jump conditions supported by the 80386DX can be tested on the PIC 16F684 using a combination of the carry (C) and zero (Z) bits. Which of the following jump conditions <u>cannot</u> be tested using simply the C and Z bits?
 - i. Equality (i.e., previous compare operation shows that two values are equal)
 - ii. Less than (i.e., previous compare operation shows the first value is less than the second)
- iii. Overflow (i.e., previous operation generated a result that cannot be represented in the number of bits available)
- iv. Not zero (i.e., previous operation generates a non-zero result)

1 (continued)

- c. Which of the following statements about shift and rotate operations using 16-bit values (and therefore multiple registers) on the PIC16F684 are <u>true</u>?
 - A. In a 16-bit left shift, the most significant byte should be shifted first to ensure that the correct data is transferred between bytes.
 - B. In a 16-bit rotate operation that does not include the carry, the initial value of the C bit should always be 0.
 - C. In a 16-bit arithmetic right shift, the initial value of the C bit should always be 1.
 - D. 16-bit rotate and shift operations with a shift amount of 1 can be done using a single PIC16F684 instruction.
 - i. Only C
 - ii. A and C
- iii. B and D
- iv. A and D
- v. None of the above (i.e., A, B, C, and D are <u>all</u> false)

- d. Which of the following statements most accurately describes how to handle 16-bit subtraction on the PIC16F684? Assume the subtraction in question is AX BX, where AX is broken into bytes AL / AH, and BX is broken into bytes BL / BH.
 - i. Subtract AL BL first, then subtract AH BH.
 - ii. Subtract AH BH first, then subtract AL BL.
- iii. Subtract AL BL first, then check the carry bit (C). If C is 1, decrement AH. Then, subtract AH BH.
- iv. Subtract AH BH first, then check the carry bit (C). If C is 1, decrement AL. Then, subtract AL BL.
- v. Stop worrying about how to do this on an 8-bit microcontroller and get yourself one that can handle a multi-byte operation in a single instruction.

(Note: (v) might be your favorite answer, it's probably not going to earn you any points.)

2. (40 points) *PIC programming sequences*

Complete each program below by writing the appropriate PIC instruction into each of the blank spaces. The purpose of each instruction is described in a comment to the right of the blank.

a. (12 points)

This short program initializes an 8-bit counter, which is stored in register COUNT, to 0x12, then count down until it reaches 0.

L:	goto L	; Decrement COUNT and ; skip next instruction if ; it's 0 ; Return to start of loop
		; Set COUNT = W = 0x12
		; Set W = 0x12

b. (12 points)

F:

This function returns a value that can be used to directly overwrite the current state of PORTC, which ensures that the following state transitions are made. Note that the lowest two bits of PORTC encode the state, the bits are shown from most significant to least significant, and the sequence repeats after 3 steps: $00 \rightarrow 10 \rightarrow 01 \rightarrow 00$

If the lowest two bits of PORTC match none of the states shown above, set those bits equal to 0. Note that the upper six bits of PORTC are unused and can therefore be overwritten with 0 values.

movf PORTC, W	; Read state of PORTC into W
	; Clear all but lowest 2 bits of W
addwf PCL, F	; Add lowest 2 bits to PCL ; to pick instruction below
retlw b'0000010'	; Go from $00 \rightarrow 10$
	; Go from 01 \rightarrow 00
	: Go from 10 \rightarrow 01
retlw b'00000000'	; Go from 11 \rightarrow 00

2 (continued)

- c. (16 points) The following short program repeatedly calls a function, ThreeVals, that uses the retlw instruction to return one of two values. The values have the following effects on the variable X:
 - If the function returns 0, the program should clear X.
 - If the function returns 1, the program should increment X.

L:	call	ThreeVals	; Call function ThreeVals
			; Test if W == 0; note that ; Z should be 1 if W == 0
			; Skip next instruction if Z == 1 ; and therefore R == 0
	goto	R1	; Handle case where $R == 1$
			; Clear x
	goto	L	; Return to start of loop
R1:			; Increment x
	goto	L	; Return to start of loop

3. (40 points, 20 points per part) Complex operations

For each of the following 80386 instructions, write a sequence of PIC 16F684 instructions that performs an equivalent operation. The operation is described in italics. <u>CHOOSE ANY TWO</u> <u>OF THE THREE PARTS</u> and fill in the space provided with appropriate code. <u>You may</u> <u>complete all three parts for up to 10 points of extra credit, but must clearly indicate which part is the extra one—I will assume it is part (c) if you mark none of them.</u>

Assume that variables with the same names are defined for all 8-bit 80386 registers (for example, "AL" and "BL"). If an operation uses a 16-bit register (e.g., AX), you can address each byte within that register (e.g. AH and AL). Also assume "TEMP" has been defined for cases where you may need an extra variable.

Note that shift or rotate operations should not be done by simply writing copies of the PIC rotate instructions. Use the shift amount provided as a literal value that will help determine the number of times you shift or rotate.

Finally, please note that <u>you are not required to write comments describing each instruction</u>. You are certainly welcome to do so if you feel it will make your solution clearer to the instructor.

a. SAR AX, 10 (Shift 16-bit value AX right by 10 bits; maintain original sign)

3 (continued) b. SETG AL

(AL = 0xFF if result of previous comparison is "greater than"; AL = 0x00 otherwise)

c. ADC AX, BX (AX = AX + BX + C)